

Patent Abstracts of Japan

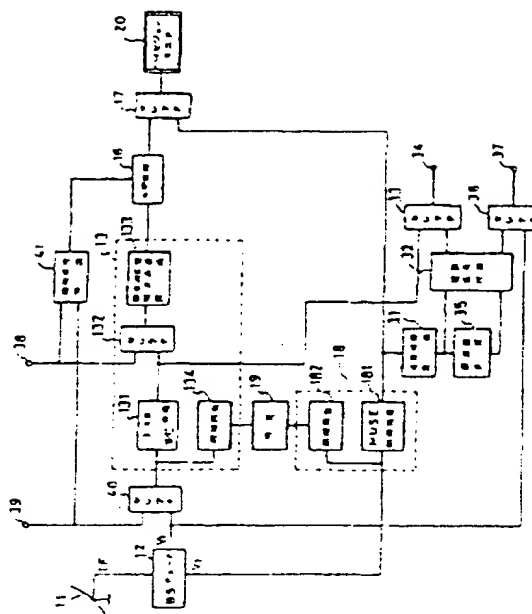
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ABSTRACT : PURPOSE: To output a high-fidelity signal, which can be recorded by a high- picture quality VTR for NTSC or a normal VTR, by providing a means to convert the scanning line number of the high-fidelity signal to the scanning line number of the NTSC system.

CONSTITUTION: The high-fidelity signal to be outputted from a MUSE video processing circuit 181 is a 2:1 jump scanning signal, for which an aspect ratio is 16:9 and the number of scanning lines is 1125, and a Y signal and a C signal are basically frequency-separated. A scanning line number converting circuit 31 converts the high-fidelity signal to a 2:1 jump scanning signal for which the number of scanning lines is 525 while maintaining the aspect ratio as it is. The format of this signal is same as the format of a EDTV signal to be outputted from a three-dimensional Y/C separating circuit 131 of an EDTV processing circuit 13 excepting for the aspect ratio. Thus, the high-fidelity signal, for which the number of scanning lines is converted by the scanning line number converting circuit 31, can be recorded by the high-picture quality VTR. The frequencies of the Y and C signals are multiplexed by a frequency multiplexing circuit 35 and the high-fidelity signal can be obtained to be recorded by the normal VTR.

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